

PATENT ABSTRACTS OF JAPAN

(11)Publication number : 2002-076786

(43)Date of publication of application : 15.03.2002

(51)Int.Cl.

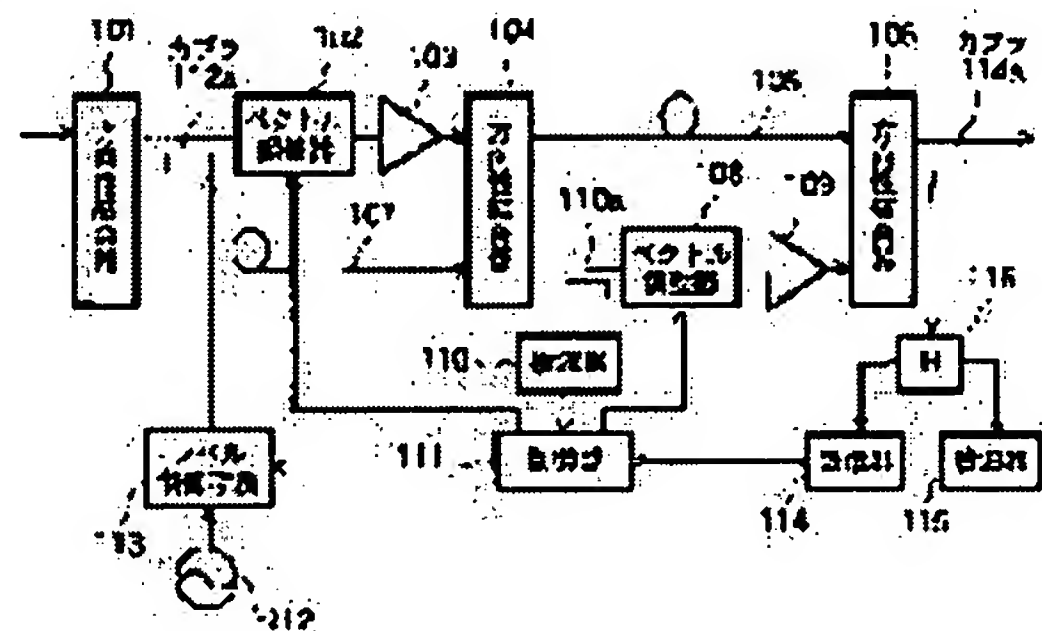
H03F 1/32
H03F 1/02
H03F 3/20(21)Application number : 2000-265813 (71)Applicant : HITACHI KOKUSAI ELECTRIC
INC(22)Date of filing : 01.09.2000 (72)Inventor : SASAYAMA TORU
SERA YASUO

(54) DISTORTION COMPENSATED AMPLIFIER

(57)Abstract:

PROBLEM TO BE SOLVED: To provide a distortion compensated amplifier which controls the level of pilot signals and allows small power consumption and spurious rejection.

SOLUTION: In the distortion compensated amplifier which detects distortion components caused by the main amplifier 103, from the first signal produced by input signals inputted into the main amplifier 103 and the second signal produced by the input signals delayed with a delay line 107, and add the components of opposite phase of the distortion components to the first signal to eliminate the distortion components from the first signal so that the output signal is made, a detector 115 which detects the level of the output signals or the input signals is provided, and the level of pilot signals to use is adjusted based on the said detected level.



LEGAL STATUS

[Date of request for examination]

[Date of sending the examiner's decision of rejection]

[Kind of final disposal of application other than the examiner's decision of rejection or application converted registration]

[Date of final disposal for application]

[Patent number]

[Date of registration]

[Number of appeal against examiner's decision of rejection]

[Date of requesting appeal against examiner's decision of rejection]

[Date of extinction of right]

Copyright (C); 1998,2003 Japan Patent Office